## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

TESTING DEVICE FOR DETECTING AND

DAVID M. TALLMAN et al.

: LOCATING ARC FAULTS IN AN

**ELECTRICAL SYSTEM** 

Filed: Herewith

: Attorney Docket No.: 01-EDP-450

## **INFORMATION DISCLOSURE STATEMENT**

March 5, 2002

Assistant Commissioner for Patents Washington, D.C. 20231

Dear Sir:

Pursuant to the provisions of 37 CFR Sections 1.56, 1.97 and 1.98, Applicants submit herewith copies of the patents and documents cited on the attached forms PTO/SB/08A and PTO/SB/08B for consideration during prosecution of the subject Application.

The Examiner's attention is directed to the commonly assigned application (Attorney Docket No. 01-EDP-451) being filed concurrently herewith, as set forth on page 1, lines 5-8 of the specification; and commonly assigned, co-pending application, entitled "Diagnostic Wiring Verification Tester," filed February 8, 2002 (Attorney Docket No. 01-EDP-299). Copies of these applications, marked "COPY" on the first sheet thereof, are enclosed for convenience of reference by the Examiner.

Additional background information is discussed in the specification under the heading "Background Information."

This Statement is filed solely for the purpose of complying with the pertinent rules of the Office and is not intended to be a substitute for an independent evaluation by the examiner of the art cited or an independent search by the examiner, and no representation of any nature is made or intended by the filing of this Statement.

In addition to the art cited on forms PTO/SB/08A and PTO/SB/08B, Applicants and/or their attorneys may have been exposed to or considered additional art relating to the general class of the subject matter of the invention. However, if in

fact such exposure or consideration has occurred, to the best of their recall or judgment, none of such art is prior art which is more relevant than the art cited.

Respectfully submitted,

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